

Remarks

The above-referenced application has been reviewed in light of the Examiner's Final Office Action dated November 2, 2005. By the present amendment, Claims 1, 2, 8, 9 and 28 have been amended. Therefore, Claims 1-10 and 28-31 are currently pending in this application. The Examiner's reconsideration of the rejections is respectfully requested, particularly in view of the above amendments and the following remarks. Claim 10 has been allowed. The Examiner's allowance of Claim 10 is gratefully acknowledged.

In accordance with the Office Action, Claims 2-7 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter that the applicants regard as the invention. Claim 2 has been amended to overcome said rejections by depending from allowed Claim 10, which does not recite an enabling switch.

In accordance with the Office Action, Claims 28 and 30 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,166,589 to Park. Claim 28 has been amended to recite features from allowed Claim 10. No new issues have been raised.

Amended Claim 28 recites, *inter alia*, "A voltage generating circuit comprising: mode means for controlling a voltage level of at least one of a first, a second and a third voltage in response to a plurality of operating mode signals ... wherein: in a low operating frequency range, first and second of the plurality of operating mode signals are at a first level; in a high operating frequency range,

the first and second of the plurality of operating mode signals are at a second level; and in an intermediate frequency range, one of the first and second of the plurality of operating mode signals is at the first level and the other is at the second level.”


As indicated by the Examiner with respect to allowed Claim 10 (see O.A. at 9), neither Park nor any of the other references of record in this case teach or suggest “A voltage generating circuit ... wherein: in a low operating frequency range, first and second of the plurality of operating mode signals are at a first level; in a high operating frequency range, the first and second of the plurality of operating mode signals are at a second level; and in an intermediate frequency range, one of the first and second of the plurality of operating mode signals is at the first level and the other is at the second level”, as currently recited by amended Claim 28.

Conclusion

The Examiner's allowance of Claim 10 is gratefully acknowledged. It is respectfully submitted that independent Claim 28 is in condition for allowance for at least the reasons stated above. Since the dependent Claims 1-9 and 29-31 each depend from the above claims and necessarily include each of the elements and limitations thereof, it is respectfully submitted that these claims are also in condition for allowance for at least the reasons stated, and for reciting additional patentable subject matter.

Therefore, Claims 1-10 and 28-31 are each in condition for allowance. All issues raised by the Examiner having been addressed, reconsideration of the rejections and an early and favorable allowance of this case are earnestly solicited.

Respectfully Submitted,

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